Problem Report PR-009

HW part: HW0001

HW rev: R002

SW rev: 2021-05-15

Test log: 2021-05-15-SPI ss and sck.sal

Description: SPI SS and SCK not aligned

Steps to reproduce the problem

1. Probe SS and SCK on the PCB
2. Power on the unit

Investigation notes

2021-05-15: After configuration, SPI SS asserts (active low) 45 us before the first falling edge of SCK (OK), but only 2us after the last rising edge of SCK (fail).

|  |
| --- |
|  |
| Screenshot of 2021-05-15-SPI ss and sck.sal |